

Lightweight Deep Learning Model for TFT-LCD Pixel Defect Detection Using MobileNetV2

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Abstract: The proposed methodology aims to achieve high classification accuracy while maintaining computational efficiency and minimizing inference latency, making it highly suitable for real-time industrial inspection scenarios. This study evaluated five traditional transfer learning models (VGG16, ResNet50, MobileNetV2, Xception, and InceptionV3) for comparison in pixel defect detection. MobileNetV2-based implementation demonstrated a competitive average test accuracy of 0.9916, outperforming several baselines while maintaining significantly lower computational complexity. Based on our investigation, MobileNetV2 offers an optimal balance between accuracy and efficiency, making it a preferred choice over heavier architectures such as VGG16 and ResNet50, which, despite achieving comparable accuracy of 1.0, incur substantially greater computational overhead and longer inference times. These results underscore the suitability of MobileNetV2 as a lightweight and reliable alternative for automated visual quality inspection in display manufacturing, particularly where real time performance and operational efficiency are critical.

Keywords: Pixel Defect, Defect Detection, Transfer Learning, MobileNetV2

Introduction

Thin-Film Transistor Liquid Crystal Displays (TFT-LCDs) are now standard components in modern life, powering displays for smartphones, televisions, laptops, and a wide range of other electronic devices (Wu et al., 2023). The ability to deliver crisp visuals, vibrant colors, and compact form factors has revolutionized how we interact with technology. However, the intricate manufacturing process of TFT-LCDs can be susceptible to various defects that compromise display quality and user experience.

TFT-LCD defects come in two categories: macro (Mura) and micro (pixel). Mura defects are larger, visible inconsistencies in brightness or color. Micro defects, like dead pixels (black dots from all transistors failing) and stuck pixels (single-color dots from one or two transistors failing), are too small to see easily (Celik et al., 2022).

Even micro defects in a TFT-LCD panel can have a significant impact. Dead pixels, where a pixel is permanently off, or bright pixels, where a pixel is constantly lit, create visible blemishes that detract from the overall aesthetics of the display. More concerning are

defects that affect color accuracy or uniformity, potentially leading to distorted or washed-out visuals. In critical applications like automotive instrument clusters, where clear and concise information display is paramount for driver safety, even slight variations in pixel performance can be disastrous. Imagine a scenario where a warning light appears, but due to a color defect, its hue is ambiguous, potentially delaying driver reaction time and jeopardizing safety (Axmann et al., 2020).

The diverse range of potential defects in TFT-LCD panels highlights the importance of robust and efficient detection methods during the manufacturing process. Early identification and elimination of defective panels not only ensure high-quality displays but also contribute to significant cost savings by preventing faulty products from reaching the final assembly stage. Traditional defect detection methods often rely on rule-based algorithms or manual visual inspection. However, these approaches have limitations. Rule-based algorithms (Nordin et al., 2025) often face challenges in keeping up with changing defect patterns, while manual inspection is laborious, error-prone, and produces inconsistent results among inspectors.

Recent advancements in deep learning, particularly in Convolutional Neural Networks (CNNs), have enabled the development of efficient and accurate methods for automated defect detection in TFT-LCD panels. CNNs excel at capturing spatial hierarchies and extracting discriminative features from image data, making them well-suited for identifying subtle pixel-level anomalies. When applied to industrial inspection systems, however, the successful deployment of CNN-based models demands careful attention to network architecture design, availability of annotated training data, and computational efficiency. These considerations become especially critical in high-throughput manufacturing environments, where real-time performance (Raiaan et al., 2023) and low-latency inference are essential.

Review of Literature

In earlier pixel defect detection methods, a notch multi-band Fourier filter (Ferreira et al., 2017) was used to locate and quantify the bright and dark dots. Qian et al. (2018) proposed a method using a quadrant gradient operator to accurately locate the sub-pixel defects. Guo et al. (2018) proposed another method using a notch filter and image registration. Moving beyond classical image processing, CNNs have become central to defect detection. This deep learning paradigm has revolutionized industrial quality control by enabling the automated, robust, and highly accurate identification of pixel-level anomalies in diverse manufacturing environments.

Deep learning has transformed computer vision, providing robust tools for automating defect detection across various industries. Among these tools, CNNs have emerged as the dominant architecture for image recognition and classification tasks. A key advantage of CNNs is their ability to automatically learn features from image data, eliminating the need for manual feature extraction. This capability is rooted in their distinctive architecture, which includes convolutional, pooling (or sub-sampling), normalization, and fully connected layers. Convolutional layers extract features from the input image, pooling layers reduce data dimensionality and introduce translation invariance, and fully connected layers perform classification or regression tasks using the learned features. By identifying latent features without human supervision, CNNs have become the most widely used algorithm in deep learning (Alzubaidi et al., 2021).

Deep neural network algorithms perform computer vision tasks such as classification, object detection, and segmentation. Unsupervised or semi-supervised learning of surface defects often utilizes object detection algorithms like Faster-RCNN, YOLO, and SSD. Çelik et al. (2021) proposed a Dead Pixel Network (DPNet) for LCD pixel detection. Celik et al. (2022) presented an image acquisition system and deep learning-based object detectors for LCD display pixel defect recognition. This method can test

displays ranging from 32 to 65 inches. The image acquisition system uses Otsu thresholding to remove the LCD panel border from raw images. The system utilizes three different deep learning object detectors for defect identification: RetinaNet with a ResNet-101 backbone, YOLOv3 with a DarkNet-53 backbone, and M2Det with a VGG-16 backbone. These detectors are evaluated using F1-Scores, Precision, and recall metrics to determine their effectiveness in defect detection.

RetinaNet achieves superior recall performance and can detect all pixel-level defective images. Lin et al. (2022) introduced DCANet, a deep channel attention-based network for MURA defect detection. It utilizes antagonistic sample collection modelling based on frequency-domain Gaussian smoothing. Their ablation study shows that DConv + CA performs effectively without increasing network parameters. The proposed DCANet outperforms state-of-the-art methods like AlexNet, VGG-16, and ResNet-50 in experiments, while also being compared to detection approaches like SSD and YOLOv3. The antagonistic training approach achieves a significant average accuracy of 95.84% and reduces the time needed for sample annotation. Notably, this method is suitable for TFT-LCD automatic inspection even with a limited training dataset of 600 samples for detecting shallow MURA defects. Chien et al. (2022) propose UNISON-ADC, a CNN-based system for automatic defect classification. Unlike shallow-structure models, UNISON-ADC utilizes multiple processing layers for image recognition tasks, reducing the number of learnable weights.

The system classifies thirteen different TFT-LCD defect patterns, focusing on finding defects in the first patterned layer during glass substrate fabrication. While the trained model achieved a good average accuracy of 93.12%, it showed lower accuracy for specific defect patterns like dummy code and particle residue. Chang et al. (2022) propose a CNN model for multi-category classification of defective pixels on TFT-LCD panels. Their experimental setup used a 15.6-inch TFT-LCD with a specific resolution, brightness, and refresh rate. The classification process involves data processing, defect classification, and classification completion. Due to computational limitations, the original images were divided into smaller pieces. Following ISO/TR 9241 standards, a small number of defective pixels of various types is considered Grade-A. However, manufacturers rarely have images with such a high defect density.

To address this, data augmentation techniques like rotation and flipping were employed to create more training images. Since each pixel has three subpixels, a total of 12 defect image types were used. Initially, the model was trained and tested with 6,000 images (80% for training and 20% for testing) encompassing 15 categories (12 defective and 3 defect-free). Due to misclassifications caused by cutting some pixels during processing, the

model initially achieved a sensitivity below 80% for specific blue defect categories. This issue was addressed by using a modified dataset with around 12,000 images. The final model achieved a minimum sensitivity of 96.97% for green half-bright defects and above 97% for all other categories, with six even reaching 100% sensitivity. Unfortunately, the classification process is time-consuming, with each panel requiring 467 seconds to screen one panel. Tomita et al. (2022) introduce a progressive hybrid model using machine learning. This model combines an 8-class CNN for multi-class classification, a 2-class ResNet for binary classification, and a separate 2-class CNN for another binary classification task. The model excels at classifying normal display images, which benefits the manufacturing process. However, its accuracy for classifying all eight defect categories requires improvement.

To address visual unevenness issues, Xie et al. (2021) proposed the UADD-GAN model for mura defect detection. In the display industry, collecting and annotating data is challenging due to the rarity of defects like mura. Song et al. (2021) thus introduced the Res-UNetGAN model to mitigate data imbalance. Liu et al. (2023) further advanced this by proposing a diffusion model for mura image generation. To ensure screen quality, Zheng et al. (2025) developed methods for high-quality defect dataset synthesis.

Experimental Setup for Dataset Preparation

An experimental platform was established for pixel-level fault emulation, comprising an Arduino Uno controlling a 2.4-inch TFT-LCD. This platform facilitates the injection of 1 to 15 random pixel defects per image via display control commands, referencing the dead pixel count ranges outlined in ISO 9241-302 Class 1, Class 2, and Class 3 panels. This experimental setup was developed to facilitate the systematic acquisition of images from a TFT-LCD module for the purpose of constructing a dataset. A REES52 Raspberry Pi camera module, interfaced with a Raspberry Pi Single-Board Computer (SBC), is mounted securely on the top surface (Pecolt et al., 2025) of a custom-built enclosure designed to isolate the display from external ambient light interference. The TFT-LCD screen is positioned at the base of the enclosure, connected via USB to a dedicated Arduino UNO for controlled image display. The enclosure ensures a consistent imaging environment, minimizing variations in lighting and reflections that could affect data quality. The camera, oriented directly above the display and connected through a flat ribbon cable to the Raspberry Pi, captures images in a top-down configuration as shown in Fig. (1). This setup was employed exclusively for dataset preparation, enabling the collection of both defective and defect-free dead pixel LCD images.

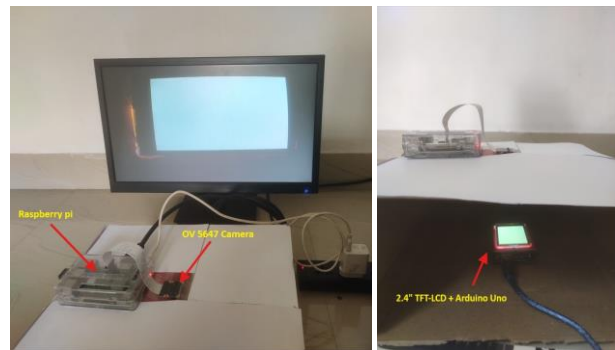


Fig. 1: Data acquisition system for TFT-LCD pixel defect

The height of the custom-built enclosure was systematically determined to ensure precise camera positioning for complete coverage of the TFT-LCD display, which was originally a vertically oriented module but was programmatically configured and physically mounted in a horizontal orientation for this setup. The display has a resolution of 240×320 pixels, and the reorientation was necessary to ensure proper alignment with the camera's field of view. In this configuration, the camera module used features a focal length of 3.6 mm and a sensor size of 3.6×2.7 mm. Based on these optical parameters, the Working Distance (WD) was calculated to be approximately 48.77 mm (Celik et al., 2022; Zheng et al., 2024). To accommodate practical considerations and ensure that the full display area, particularly the four corner edges where defects were intended to be displayed, was entirely within the frame, a small margin of approximately 2 cm was added to the calculated WD. This adjustment helped to guarantee uniform and consistent capture of the entire active screen area while preserving image quality and minimizing distortion. The enclosure effectively eliminated ambient light interference, ensuring optimal lighting conditions for quality image acquisition suitable for dataset preparation.

Due to the rarity of defective pixels in real-world scenarios and the impractical time frame spanning several months required to collect a sufficient sample, we programmatically simulated both defect-free and defective images to ensure dataset balance. Defect-free images across red, green, blue, and white backgrounds underwent augmentation via uniform RGB reduction (0.1–5%) with tone preservation, repeated 125 times each. For defective images captured against red, green, blue, and white backgrounds, pixel-level faults were emulated over 125 iterations per background, with each iteration randomly generating 1–15 dead pixels in both count and spatial location.

After collecting both defect-free and defective images using a Pi camera module interfaced with a Raspberry Pi SBC, we performed data augmentation (Kumar et al., 2024) to further balance the dataset. Specifically, flipping

was applied to each category, increasing the number of images per class to 500. As a result, a total of 1000 images were prepared for model training.

To aid in the visual interpretation of single-pixel anomalies, representative sample images are selected and cropped to highlight pixel-level behaviour. As illustrated in Fig. 2, this visualization reinforces the spatial clarity of the simulated faults.

MobileNetV2 Architecture

MobileNetV2 is a lightweight and computationally efficient convolutional neural network architecture specifically designed for resource-constrained environments such as mobile and embedded devices (Sandler et al., 2018). Its core innovation lies in the use of inverted residuals and linear bottlenecks, allowing feature reuse across layers while preserving representational power. Each bottleneck block consists of a sequence: pointwise convolution, depth wise convolution, and another pointwise convolution without a non-linearity (i.e., a linear projection).

MobileNetV2 replaces standard convolutions with a combination of depthwise and pointwise convolutions, known collectively as depthwise separable convolutions. A depthwise convolution applies a single filter per input channel, drastically reducing the number of required operations. This is followed by a pointwise convolution (a 1×1 convolution), which performs channel-wise mixing and enables feature fusion across channels. This factorization greatly reduces computational complexity compared to traditional convolutions. Additionally, the architecture uses ReLU6 activation, which limits the output to a maximum value of 6, making it more robust for low-precision operations and further enhancing compatibility with quantization techniques. The MobileNetV2 architecture can be seen in Fig. (3).

In transfer learning, MobileNetV2 serves as a powerful feature extractor by leveraging pre-trained weights from large-scale datasets like ImageNet. The base model is typically frozen to retain learned representations, and a custom classification head (Gulzar, 2023) is used. This head commonly includes Global Average Pooling, followed by one or more Dense layers with ReLU activation, and a final output layer suited to the specific classification task (e.g., sigmoid or softmax). This modular design allows efficient adaptation to new tasks while retaining MobileNetV2's speed and compactness.

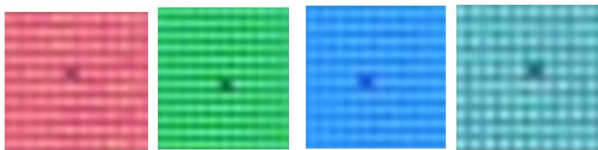


Fig. 2: Cropped Visualization of a Single Dead Pixel Across Backgrounds

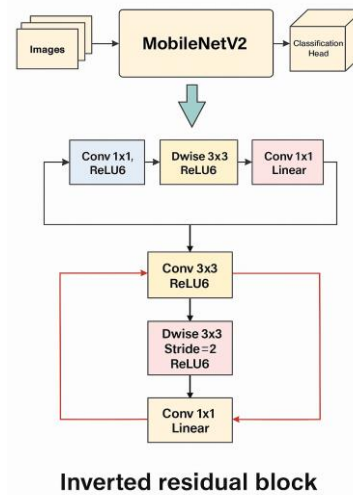


Fig. 3: Transfer Learning MobileNetV2 Architecture

Proposed Methodology

This study evaluated five traditional transfer learning models (VGG16, ResNet50, MobileNetV2, Xception, and InceptionV3) for comparison in pixel defect detection. For each base architecture, all layers were initially frozen to utilize pre-trained ImageNet feature representations. A tailored classification head was subsequently attached to the frozen backbone, comprising a global average pooling layer followed by dense-dropout-dense layers, with the output dense layer employing sigmoid activation for binary classification.

Comprehensive hyperparameter tuning was performed, systematically varying batch sizes (16, 32, 64, 128), activation functions (Tanh, ReLU, PReLU, LeakyReLU), and optimizers (Adagrad, SGD, Adamax, RMSprop, Adam, Nadam). Overfitting was addressed through evaluation of dropout rates (0.6, 0.5, 0.4, 0.3) alongside learning rates (0.01, 0.05, 0.001, 0.005) (Raiaan et al., 2024; Tuba et al., 2021). Initial comparative training across all architectures utilized the Adam optimizer over 40 epochs. Hyperparameters were set with a batch size of 32, ReLU activation, a 0.5 dropout rate, and a learning rate of 0.0001 optimized through prior tuning to balance stable performance and convergence. Early stopping with patience = 3 halted training after three consecutive validation epochs without improvement.

To assess the impact of dataset partitioning on model performance, various split ratios were tested using the lightweight MobileNetV2 architecture, and the LCD background color is red. The outcomes of these experiments are summarized in Table (1). Among the evaluated configurations, the 70/15/15 split produced the highest test accuracy (0.9733) and the lowest test loss (0.0499), indicating superior generalization. Allocating 70% of the data for training provides sufficient samples for

stable feature learning, while the balanced 15% validation and 15% test sets ensure reliable hyperparameter tuning and unbiased performance estimation. This split therefore offers the optimal trade-off between learning capacity and evaluation robustness.

This study developed the model using Python 3.9 and the TensorFlow framework, with Jupyter Notebook employed for code execution and model training. The experiments were conducted on a computational platform equipped with an Intel Core i7-14700 K processor (20 cores, 3.40 GHz), 32 GB of DDR5-5200 RAM, an NVIDIA GeForce RTX 4060 Ti with 8 GB of VRAM, and running Windows 10.

Table 1: Impact of Dataset Partitioning on MobileNetV2 Performance Metrics

Split ratio (Train/ Validation/Test)	Test Accuracy	Test Loss
60/20/20	0.9850	0.07204
70/10/20	0.9850	0.0519
70/20/10	0.9900	0.0542
70/15/15	0.9933	0.0499

Results and Discussion

This section presents a comprehensive performance comparison between the other models and our proposed model. The evaluation emphasizes the strengths, limitations, and intended applications of each model, highlighting how architectural choices and task definitions impact their suitability for defect detection tasks. Table (2) shows a comparative performance analysis between five transfer learning models.

Although the VGG16 and ResNet50 baseline models demonstrated exceptionally high performance (achieving 1.0 test accuracy across all background screen datasets),

our proposed MobileNetV2 model offers a superior and optimal balance between accuracy and computational efficiency, making it the preferred choice over these heavier architectures for real-world deployment. It achieved an average testing accuracy of 0.9916. Table (3) shows the performance metrics of our proposed model.

Based on the investigation of the model's accuracy and loss curves for the proposed architecture, the rapid initial climb is characteristic of transfer learning, particularly when employing a frozen backbone. The pre-trained weights provide a highly effective initialization, enabling the newly added classification head to quickly map these high-level features to the target classes. In all accuracy and loss curve plots, no widening gap is observed between the training and validation curves for any background-screen dataset. The accuracy and loss curves of the proposed model across all background-screen datasets are shown in Fig. (4-7).

The model described in Celik et al. (2021) is optimized for real-time industrial applications, particularly in production lines where rapid identification and classification of defective pixels are critical. Its focus lies in both defect classification and pixel localization. While its primary objective differs from our proposed method especially given our emphasis on classification rather than localization—we compare key evaluation metrics and model complexity, such as the number of parameters, to provide useful context regarding their respective efficiencies. In their proposed RetinaNet, which uses ResNet101 as the backbone, they reported a precision of 0.950, a recall of 0.997, and an F1-score of 0.973.

Table 2: Comparative performance analysis between five transfer learning models

Models	Screen Color	Test Accuracy	Test Loss	Confusion Matrix	No. Of parameters
VGG 16	Red	1.0	0.0158	[[75 00 75]]	138 M
	Green	1.0	0.0296	[[75 00 75]]	
	Blue	1.0	0.0107	[[75 00 75]]	
	White	1.0	0.0098	[[75 00 75]]	
Res Net 50	Red	1.0	0.0147	[[75 00 75]]	25.6 M
	Green	1.0	0.0064	[[75 00 75]]	
	Blue	1.0	0.0042	[[75 00 75]]	
	White	1.0	0.0057	[[75 00 75]]	
Mobile NetV2	Red	0.9933	0.0499	[[74 1075]]	3.5 M
	Green	0.98	0.0875	[[72 30 75]]	
	Blue	1.0	0.0534	[[75 00 75]]	
	White	1.0	0.0618	[[75 00 75]]	
Xception	Red	1.0	0.0373	[[75 00 75]]	22.9 M
	Green	0.9533	0.1539	[[69 61 74]]	
	Blue	0.9733	0.0608	[[71 40 75]]	
	White	0.867	0.3012	[[63 128 67]]	
InceptionV3	Red	0.9933	0.0439	[[74 10 75]]	23.9 M
	Green	0.9533	0.1216	[[63 70 75]]	
	Blue	0.9867	0.0705	[[73 20 75]]	
	White	0.967	0.0998	[[73 23 72]]	

Table 3: Performance metrics of MobileNetV2 trained on images with varying background conditions

Screen Color	Test Accuracy	Test Loss	Precision	F1 Score	Sensitivity
Red Defective Free	0.986	0.0571	0.986	0.986	0.986
Red Defective			0.986	0.986	0.986
Green Defective Free	0.98	0.0875	1.0	0.96	0.979
Green Defective			0.961	1.0	0.980
Blue Defective Free	1.0	0.0530	1.0	1.0	1.0
Blue Defective			1.0	1.0	1.0
White Defective Free	1.0	0.0618	1.0	1.0	1.0
Red Defective			1.0	1.0	1.0

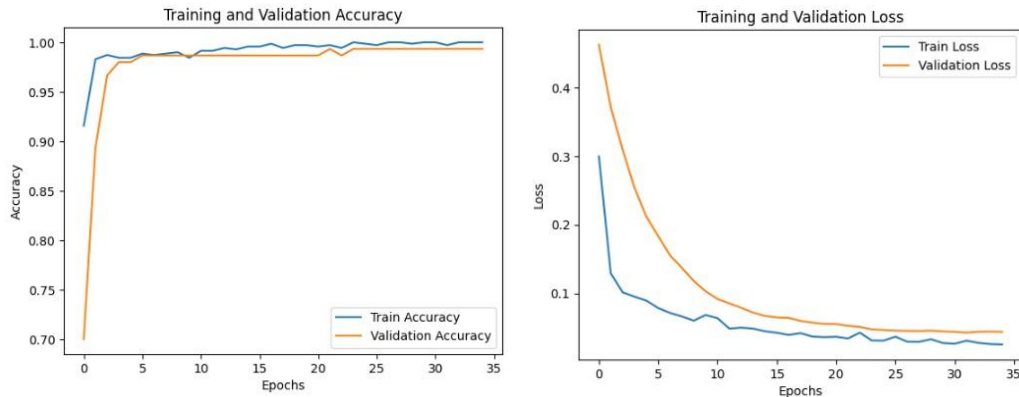


Fig. 4: Accuracy and loss curves for MobileNetV2 trained on red background images

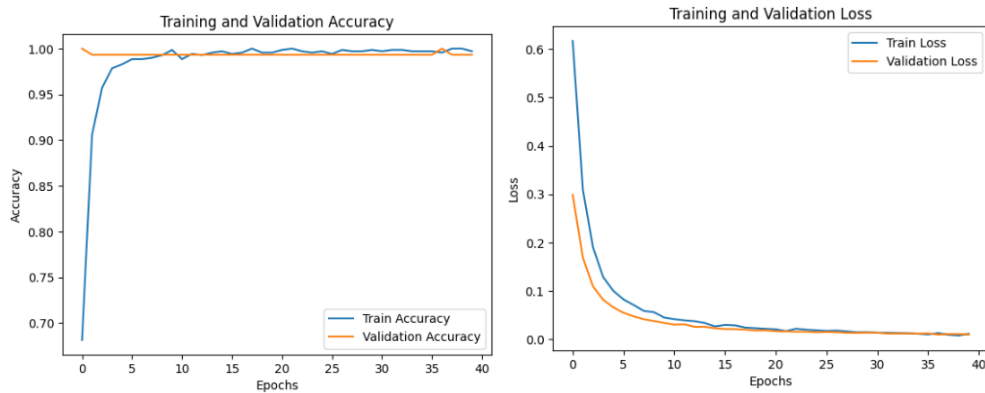


Fig. 5: Accuracy and loss curves for MobileNetV2 trained on green background images

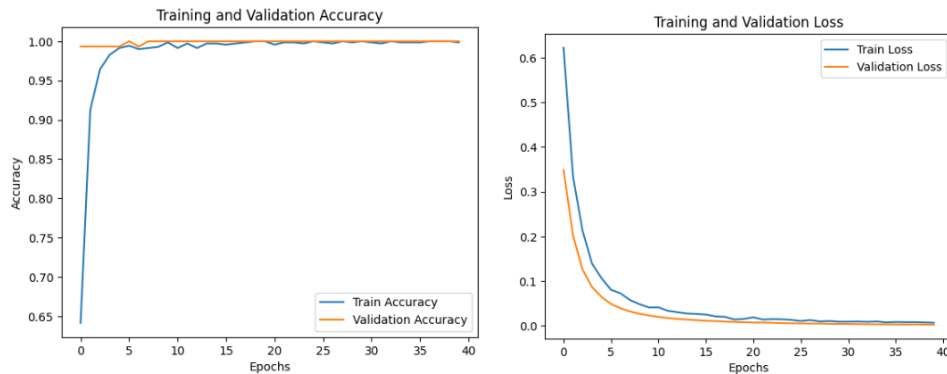


Fig. 6: Accuracy and loss curves for MobileNetV2 trained on blue background images

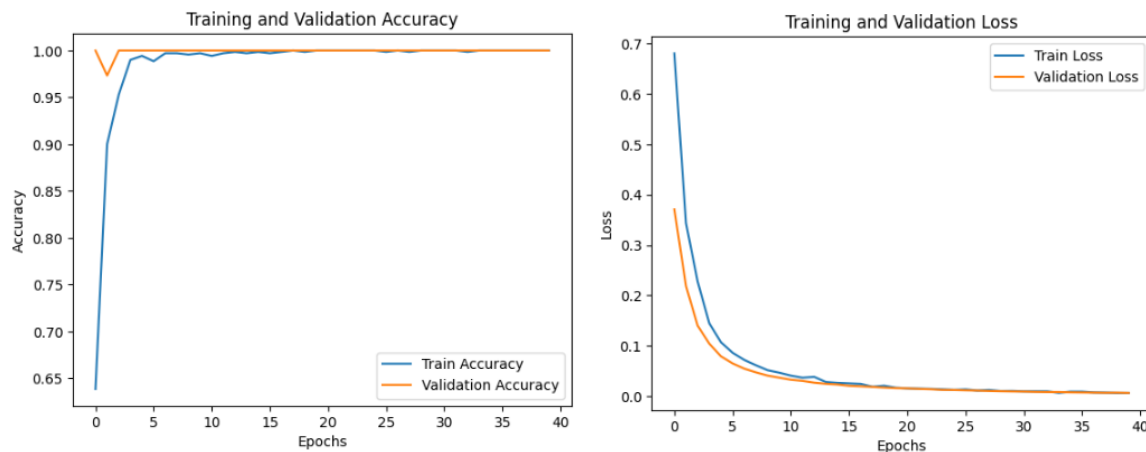


Fig. 7: Accuracy and loss curves for MobileNetV2 trained on white background images

In contrast, the method in Chang et al. (2022) bears a greater resemblance to our proposed binary classification model. However, a direct comparison of parameter count was not feasible due to insufficient details provided in Chang et al. (2022). They reported defect categories as full-dark defect, half-dark defect, full-bright defect, half-bright defect, and defect-free across red, green, and blue screen colors. However, for our analysis, we specifically investigated their model's sensitivity to two categories: full-dark defect and defect-free, evaluated across red, green, and blue screens. Their model achieved the following sensitivities: 0.884 for the blue full-dark defect, 0.947 for the green full-dark defect, and 0.8657 for the red full-dark defect.

Collectively, our findings validate the practical advantages of our proposed method, which outperforms the existing models, particularly in scenarios demanding real-time, efficient, and high-performing pixel defect classification.

Conclusion

This study introduces a lightweight and efficient model for detecting pixel-level defects in TFT-LCD panels, optimized for real-time industrial inspection scenarios. The proposed architecture was benchmarked against the complex VGG16 and ResNet50 models and other existing solutions using a range of evaluation metrics. Despite its reduced complexity, our model demonstrates competitive classification accuracy, while significantly improving computational efficiency and reducing inference latency. These characteristics make it particularly well-suited for deployment in practical manufacturing environments where speed and resource constraints are critical. Future research will focus on extending the current framework beyond classification to jointly address feature extraction, classification, and bounding-box regression on low-power platforms such as

embedded and edge devices. In addition, this work will be further continued to detect a broader range of defects in TFT-LCD panels, with the goal of scientifically strengthening quality inspection processes and improving industrial product reliability.

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Author's Contributions

J. Sheikshabjan: Contributed to conceptualization, Methodology, Software design, validation, writing of the original draft, preparation, review, and editing.

K. Dhanakodi: Contributed to supervision, review, and finalizing the manuscript.

A.T. Rajamanickam: Contributed to project administration and visualization

C. Josephine ArockiaMary and S. Athinarayanan: Contributed to data curation and investigation.

Ethics

The authors reiterate their commitment to promptly and transparently addressing any ethical issues that may be identified after publication, including taking appropriate corrective actions where necessary.

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